

FIG. 1A

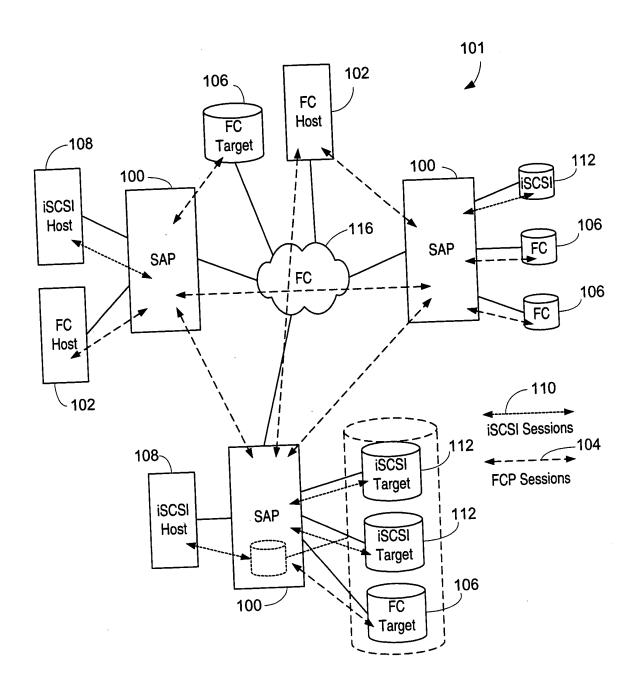


fig. 1B

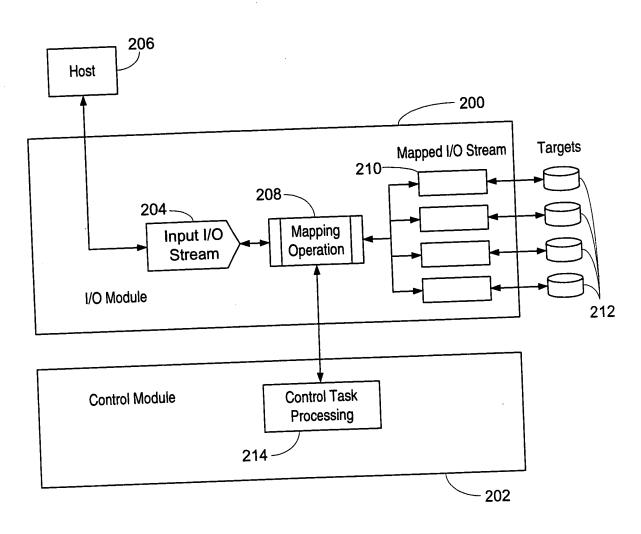


fig. 2

Software

Industry APIs Topology & Discovery Routines Network Management

Fibre Channel Connectivity
Processor
IP Connectivity Processor
Management Processor

Firmware

I/O Processor Port Processors

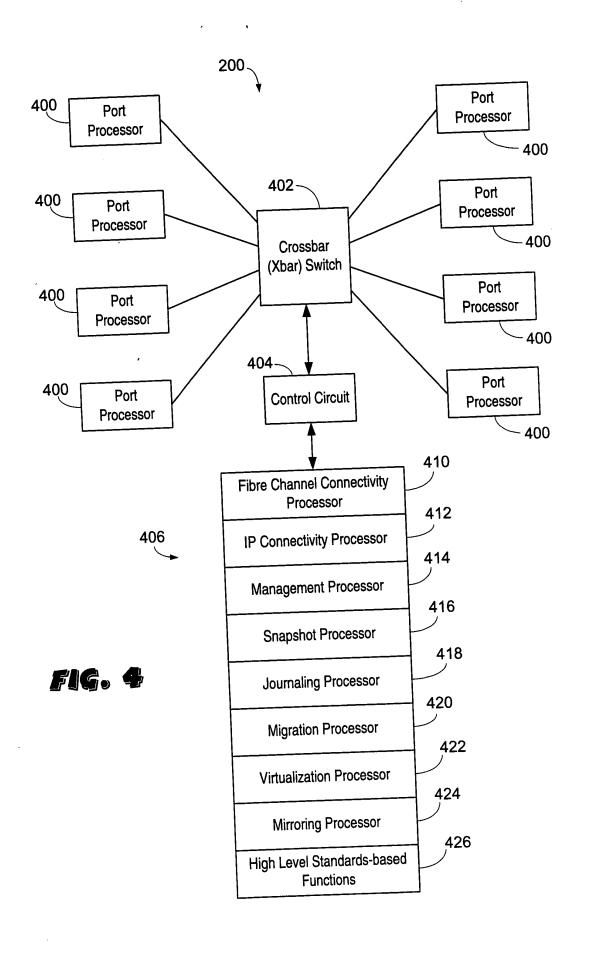
Custom Application Segments Virtualization Engine

Crossbar Switch

Silicon

Custom Application Segments





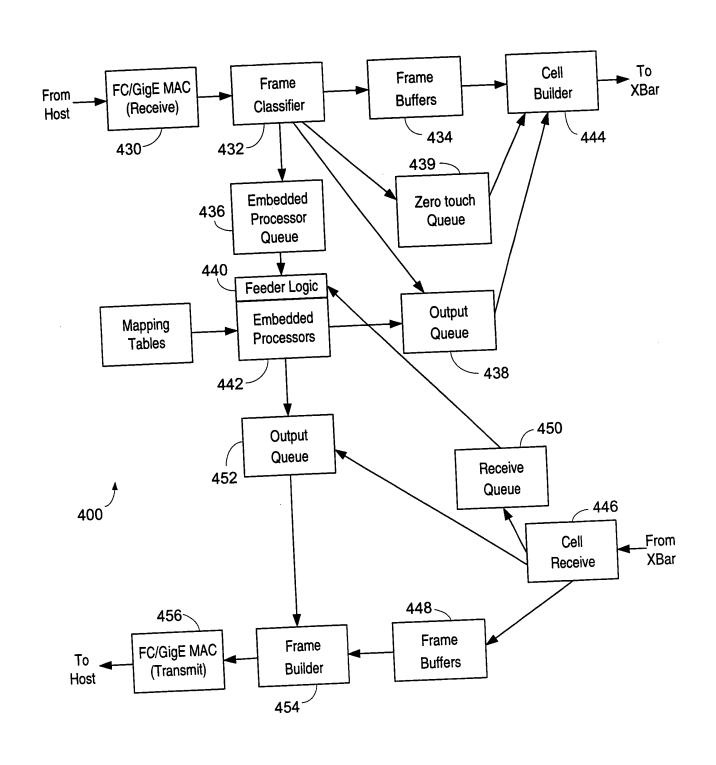


fig. 5

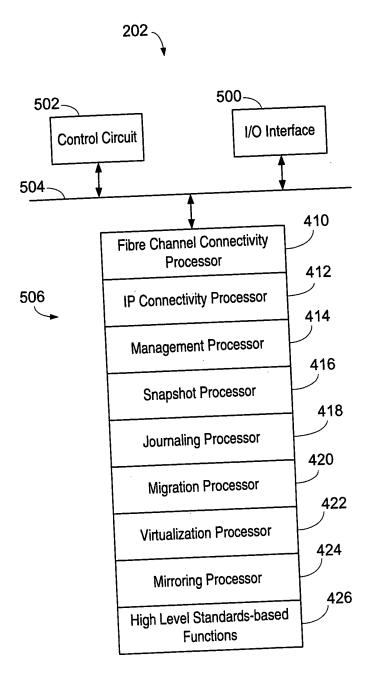


fig. 6

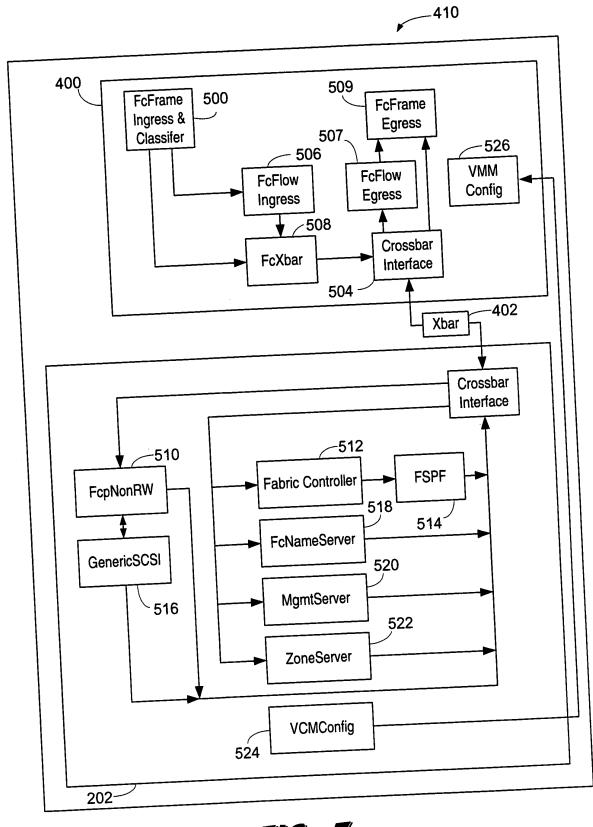


fig. I

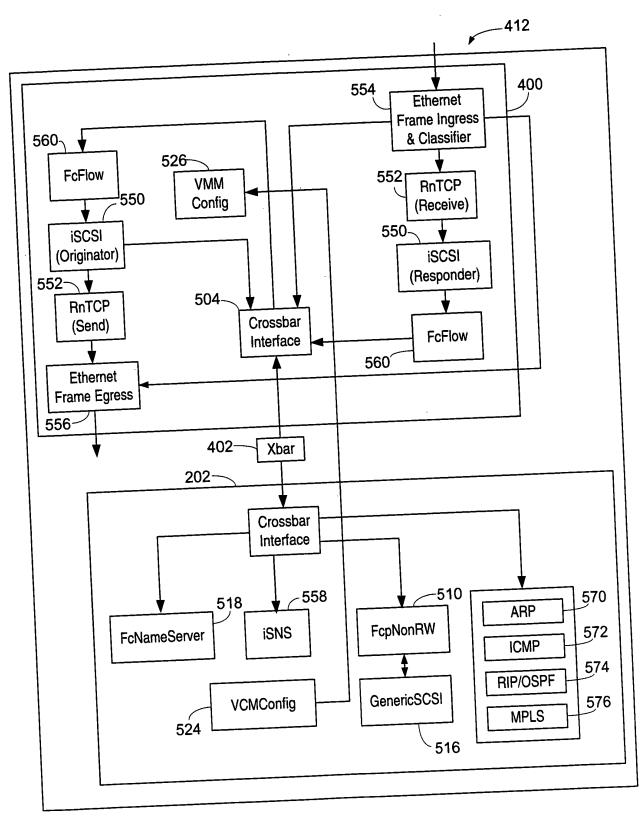
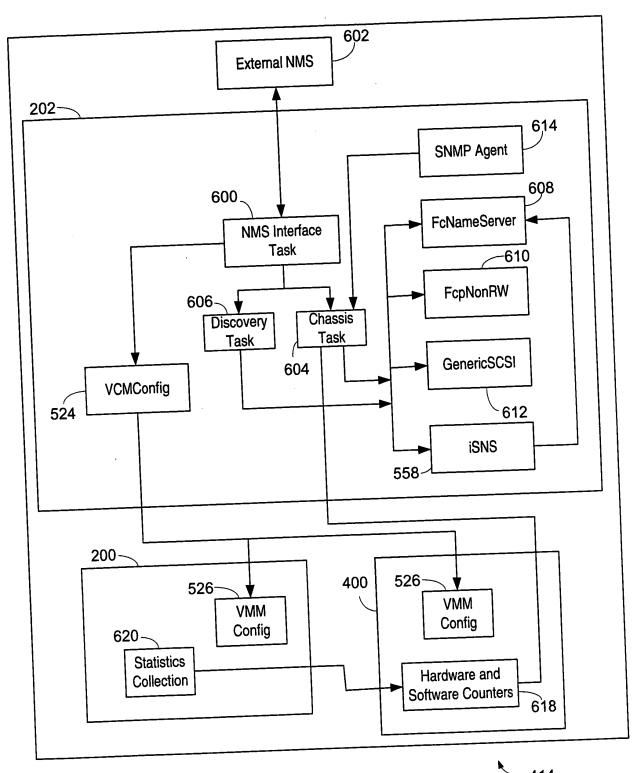
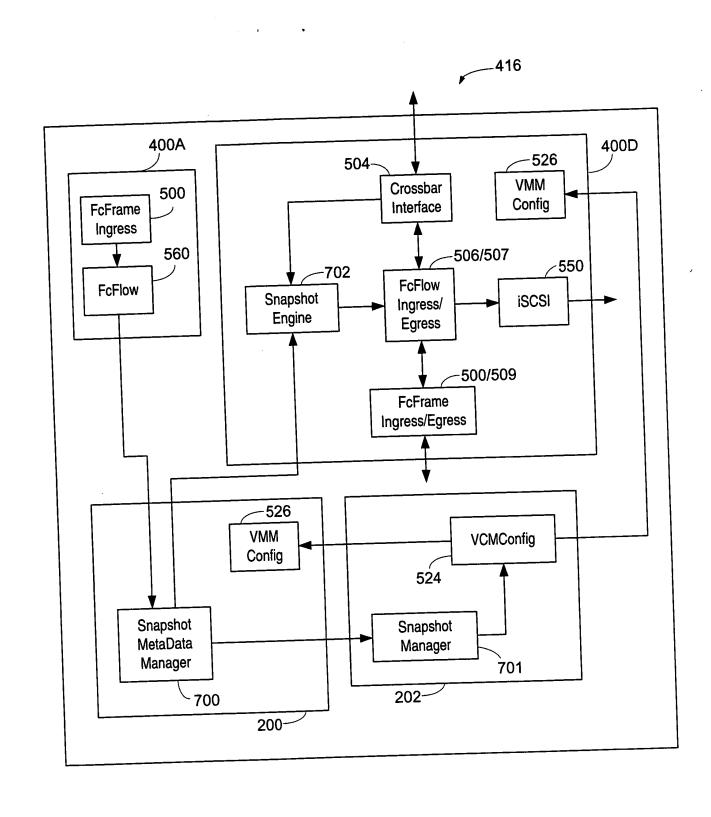


fig. 8



-414

fig. 9



f1G. 10

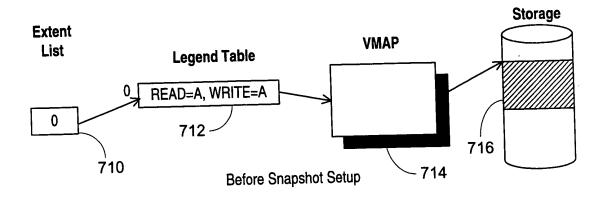


fig. 11

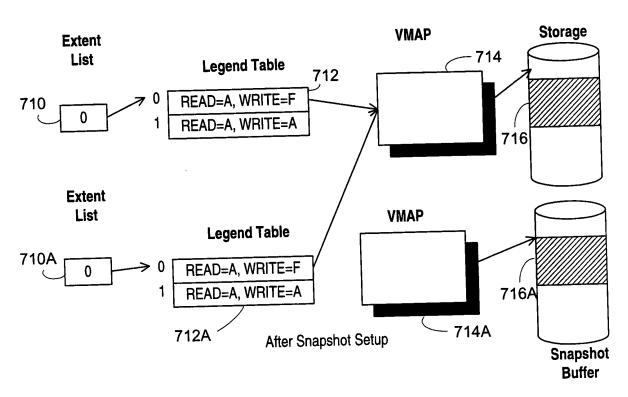


FIG. 12

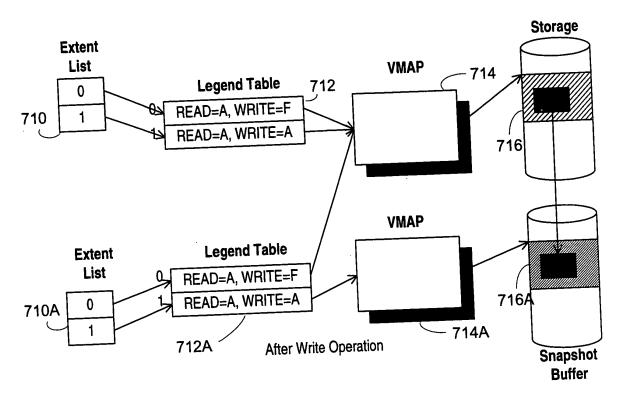
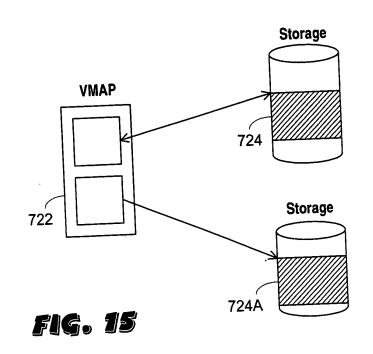
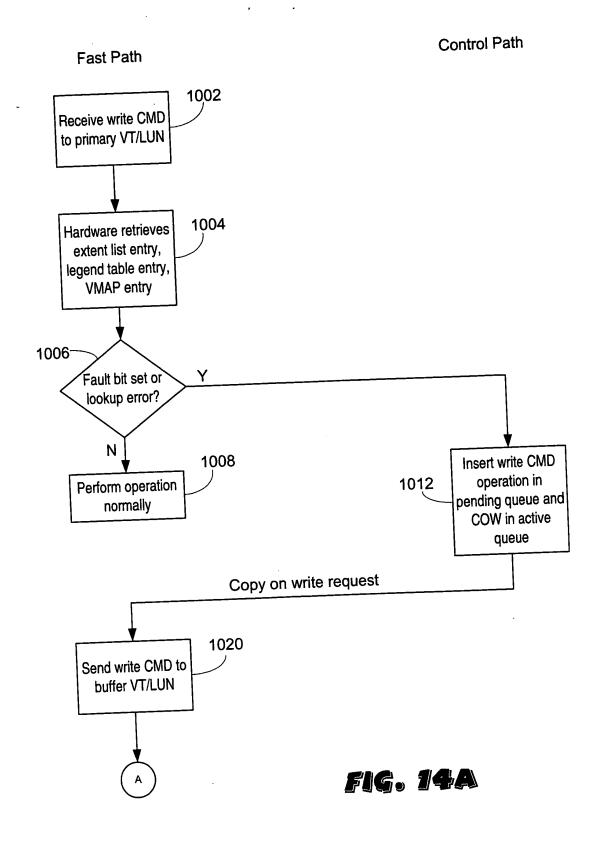
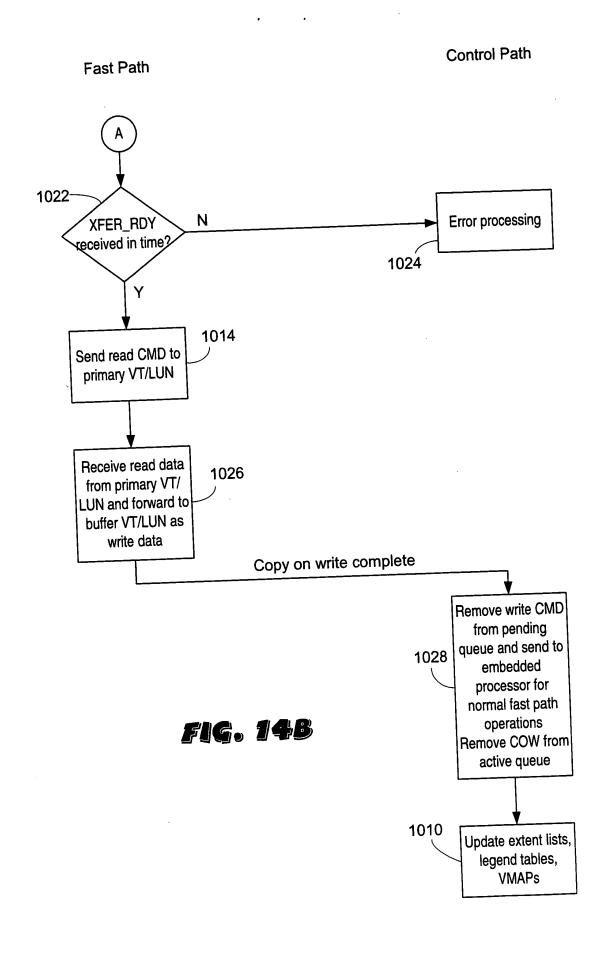
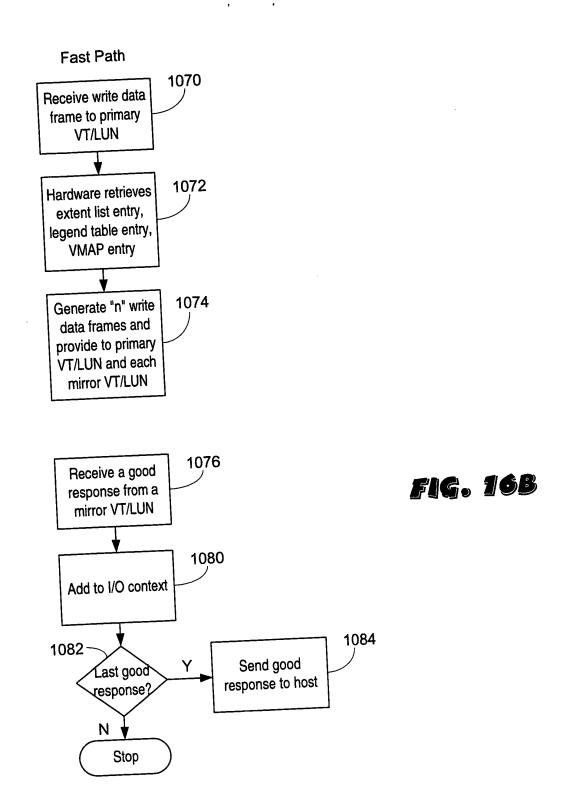


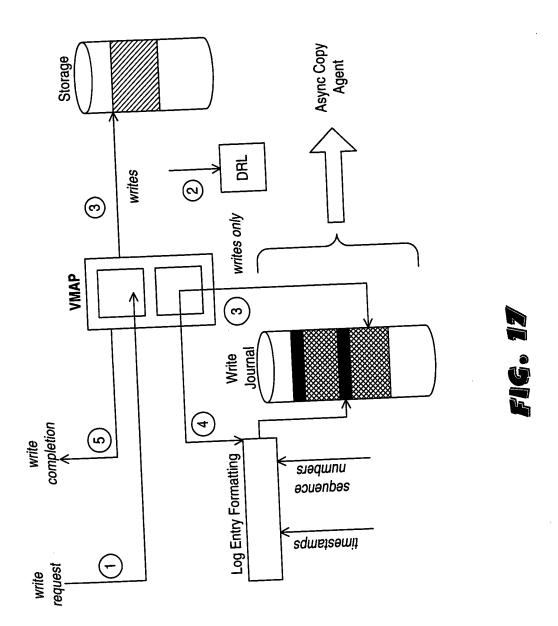
FIG. 13



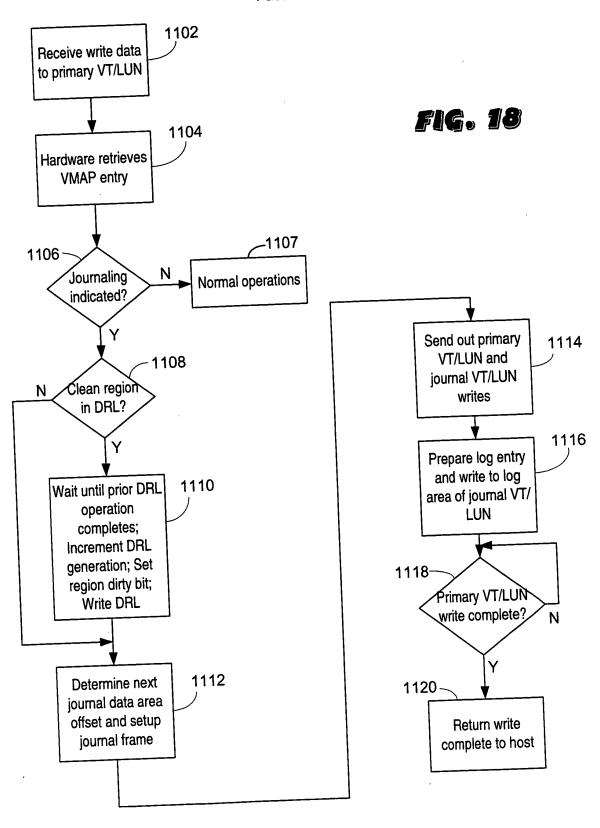








Fast Path



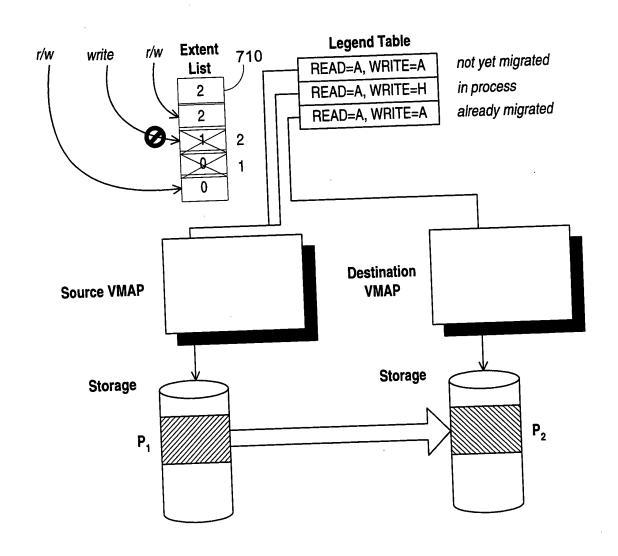
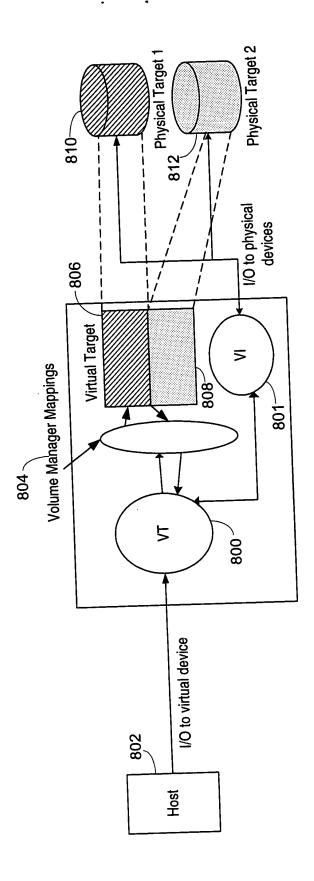
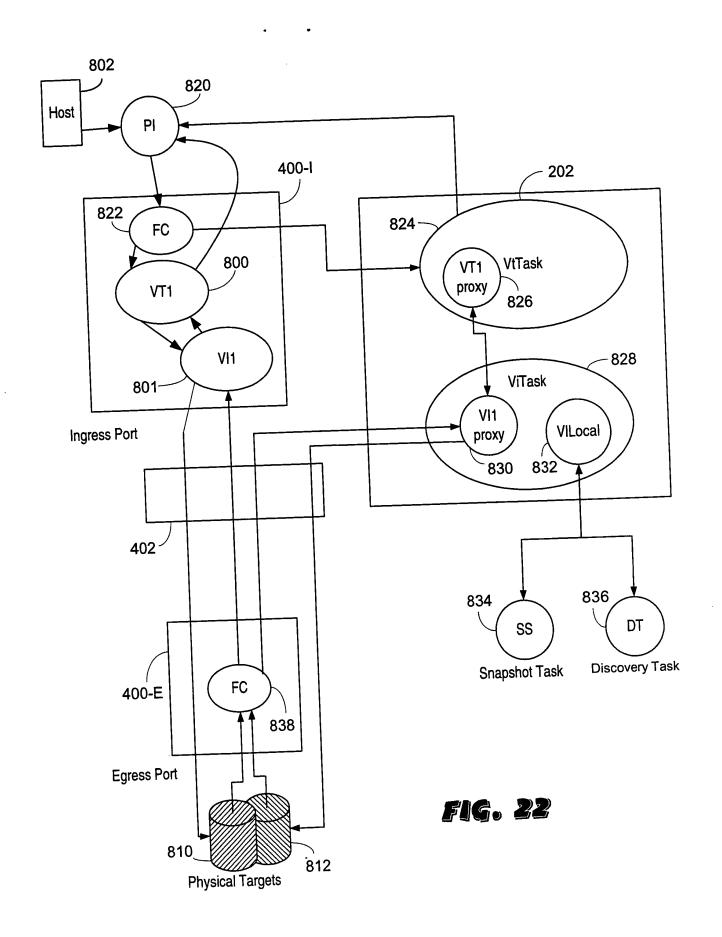


fig. 20A

fig. 20B





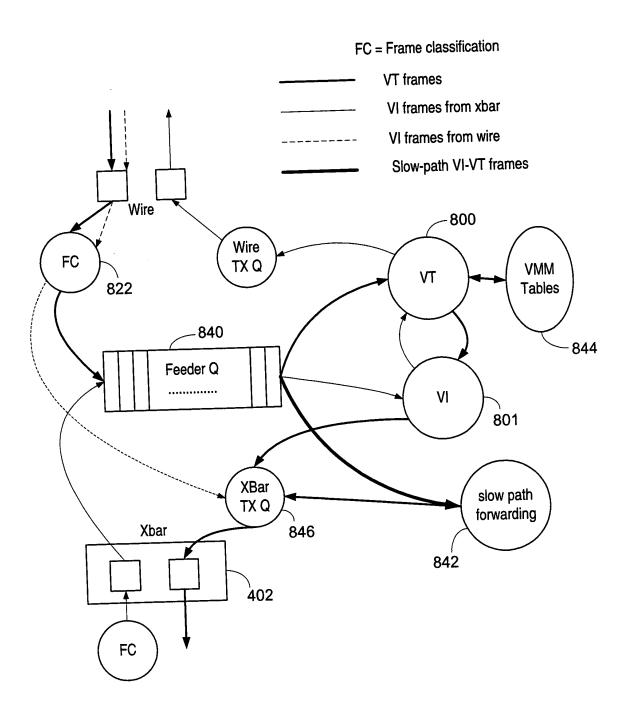
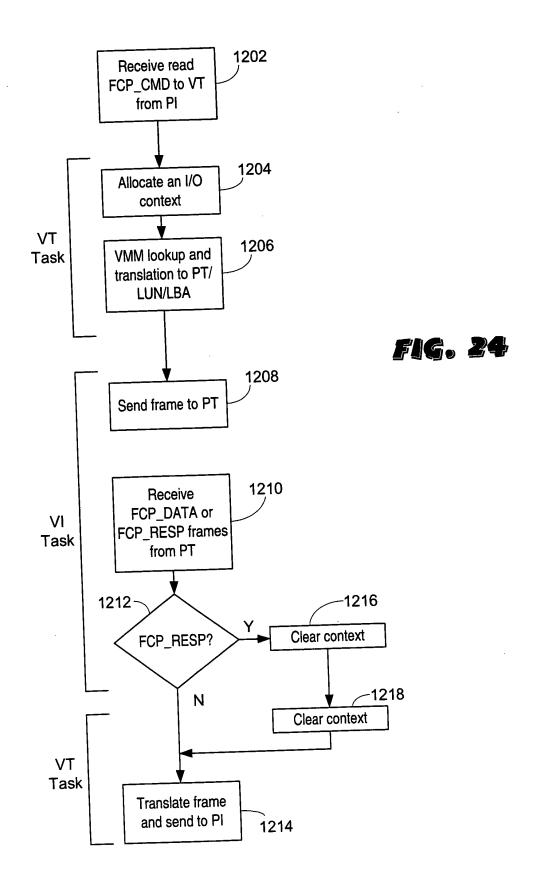


FIG. 23



Simple Write

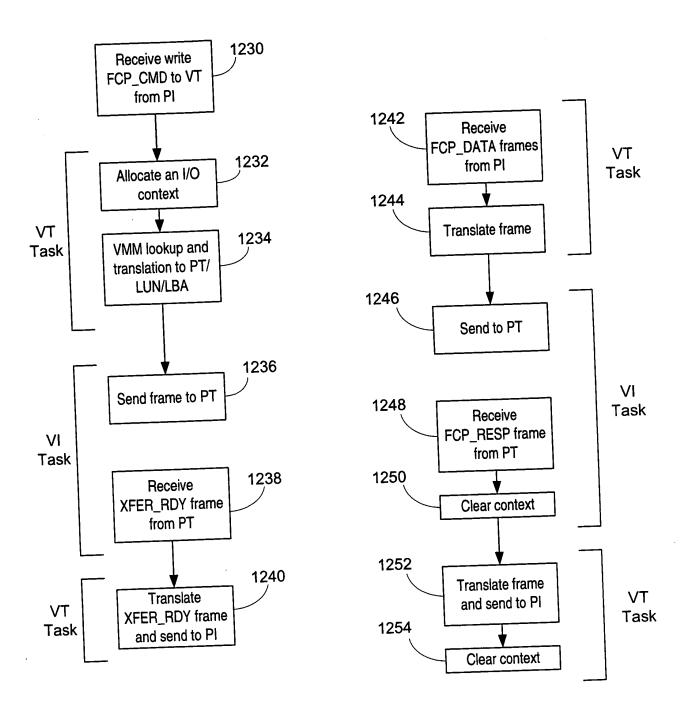


FIG. 25

Spanned Read

